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## U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10003748	FILING DATE 10/23/2001	CLASS 257	SUBCLASS 79	GAU 2814	EXAMINER D. WILLE
<b>**APPLICANTS:</b> Yabusaki Keiichi; Ohkubo Michio:					
<b>**CONTINUING DATA VERIFIED:</b>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> JAPAN 2000-399425 12/27/2000 ✓					
PG-PUB		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials <i>D. Wille</i>				ATTORNEY DOCKET NO 6635-60418	
TITLE : Semiconductor devices, and methods of manufacture of the same <small>U.S. DEPT. OF COMMERCE PAT. &amp; TM. PTO-422 (Rev. 12-94)</small>					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims _____ Print Claim for O.G. _____	
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig. _____	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		Primary Examiner _____ Application Examiner _____	
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